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6/3/03

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of:

Divakaruni et al.

Serial No. 09/718,850

Filed: November 11, 2000

Group Art Unit: 2811

Examiner: Gebremariam, Samuel A.

For: LOGIC STRUCTURE PROCESS AND APPLICATION FOR VERTICLE BIPOLAR TRANSISTOR

Assistant Commissioner of Patents  
Washington, D.C. 20231

FAX RECEIVED

SECOND AMENDMENT UNDER 37 C.F.R. §1.111

MAY 29 2003

TECHNOLOGY CENTER 2800

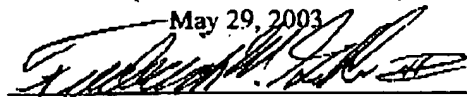
Sir:

In response to the Office Action mailed January 22, 2003 and the Notice of Non-Compliance dated May 16, 2003, please amend the above-identified patent application as follows:

Certificate of Transmission by Facsimile

I hereby certify that this correspondence is being facsimile transmitted to the United States Patent and Trademark Office (Fax No. 703-872-9318) on

May 29, 2003



Frederick W. Gibb, III